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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: : Docket No.: IL920030060US1
Monica Farkash, et al. : Examiner:
Serial No.: 10/813,239 : Group Art Unit: 2825
Filed: March 30, 2004
For: DISTRIBUTED BDD REORDERING

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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Inventor Patent Application of:

Monica Farkash, et al.

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: Confirmation No.: 4828

: Group Art Unit: 2825

: Examiner:

: Date: September 8, 2004

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under 37 C.F.R. 1.97(b)(3)**

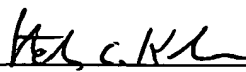
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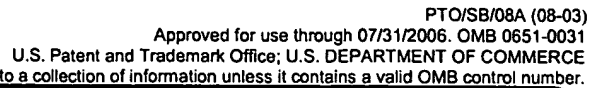
In accordance with 37 CFR 1.97(b), this information disclosure statement is being filed within three months of the filing date of the application or before the mailing date of the first Office Action on the merits, whichever event occurs last. **Please charge any fee necessary to enter this paper to deposit account 09-0468.**

Respectfully submitted,
Attorney for the Applicant(s)


By Stephen C. Kaufman
Registration No.: 29,551

IBM Corporation
Intellectual Property Law Department
P. O. Box 218
Yorktown Heights, N. Y. 10598
Telephone No.: (914) 945-3197
Fax No.: (914) 945-3281

IBM Docket: IL920030060US1



Application Number	10/813239
Filing Date	30-Mar-2004
First Named Inventor	Farkash, Monica
Art Unit	2825
Examiner Name	
Attorney Docket Number	IL920030060US1

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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/813239
Filing Date	30-Mar-2004
First Named Inventor	Farkash, Monica
Art Unit	2825
Examiner Name	
Attorney Docket Number	IL920030060US1

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	D	Rudell in "Dynamic Variable Ordering for Ordered Binary Decision Diagrams," Proceedings of the IEEE International Conference on Computer-Aided Design (Santa Clara, California,	
	E	Song and Chang "A Variable Reordering Method for Fast Optimization of Binary Decision Diagrams," Proceedings of the Fifth Asian Test Symposium ATS'97 (IEEE, 1997), pp. 228	
	F	Bryant and Meinel "Ordered Binary Decision Diagrams in Electronic Design Automation," published in Logic Synthesis and Verification (Kluwer Academic Publishers, 2001), pages	
	G	Meinel and Slobodova "Speeding Up Variable Reordering of OBDDs," Proceedings of the International Conference on Computer Design (1997), pages 338-343	
	H	Beer et al., in "RuleBase: an Industry-Oriented Formal Verification Tool," Proceedings of the Design Automation Conference DAC'96 (Las Vegas, Nevada, 1996)	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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